

In re Patent Application of

KIMURA et al

Serial No. 10/668,166

Filed: September 24, 2003

Title: SEMICONDUCTOR DEVICE AND CHIP-STACK SEMICONDUCTOR DEVICE



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Atty Dkt. 1035-473

C# M#

C/A.U. 2811

Examiner: Arena, Andrew O.

Date: February 2, 2006

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**RESPONSE/AMENDMENT/LETTER**

This is a response/amendment/letter in the above-identified application and includes an attachment which is hereby incorporated by reference and the signature below serves as the signature to the attachment in the absence of any other signature thereon.

☐ **Correspondence Address Indication Form Attached.**

**Fees are attached as calculated below:**

Total effective claims after amendment **20** minus highest number  
previously paid for **20** (at least 20) = 0 x \$50.00 \$0.00 (1202)/\$0.00 (2202) \$

Independent claims after amendment **4** minus highest number  
previously paid for **5** (at least 3) = 0 x \$200.00 \$0.00 (1201)/\$0.00 (2201) \$

If proper multiple dependent claims now added for first time, (ignore improper); add  
\$360.00 (1203)/\$180.00 (2203) \$

Petition is hereby made to extend the current due date so as to cover the filing date of this  
paper and attachment(s)  
One Month Extension \$120.00 (1251)/\$60.00 (2251)  
Two Month Extensions \$450.00 (1252)/\$225.00 (2252)  
Three Month Extensions \$1020.00 (1253)/\$510.00 (2253)  
Four Month Extensions \$1590.00 (1254)/\$795.00 (2254)  
Five Month Extensions \$2160.00 (1255)/\$1080.00 (2255) \$

Terminal disclaimer enclosed, add \$130.00 (1814)/\$65.00 (2814) \$

☐ Applicant claims "small entity" status. ☐ Statement filed herewith

Rule 56 Information Disclosure Statement Filing Fee \$180.00 (1806) \$

Assignment Recording Fee \$40.00 (8021) \$

Other: \$

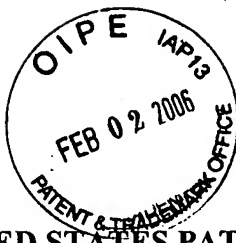
**TOTAL FEE ENCLOSED \$ 0.00**

The Commissioner is hereby authorized to charge any deficiency, or credit any overpayment, in the fee(s) filed, or asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Account No. 14-1140. A duplicate copy of this sheet is attached.

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NIXON & VANDERHYE P.C.  
By Atty: Joseph A. Rhoa, Reg. No. 37,515

Signature: \_\_\_\_\_



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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Atty. Ref.: 1035-473; Confirmation No. 4031

Appl. No. 10/668,166

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For: SEMICONDUCTOR DEVICE AND CHIP-STACK SEMICONDUCTOR DEVICE

\* \* \* \* \*

February 2, 2006

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Alexandria, VA 22313-1450

Sir:

**AMENDMENT**

Responsive to the Official Action dated November 3, 2005, please amend the  
above-identified application as follows: